

Abstract of the Disclosure

The present invention is directed to a method of reducing power consumption by a video display. The method employs time-based subsampling of an input video image, at a rate slower than that of conventional computer monitors. A control circuit allows frame-based, line-based and pixel-based subsampling. The slower rate is achieved by utilizing analog, digital or both analog and digital storage elements to hold the input video information for a time sufficient to prevent flicker and other degradation of the image.